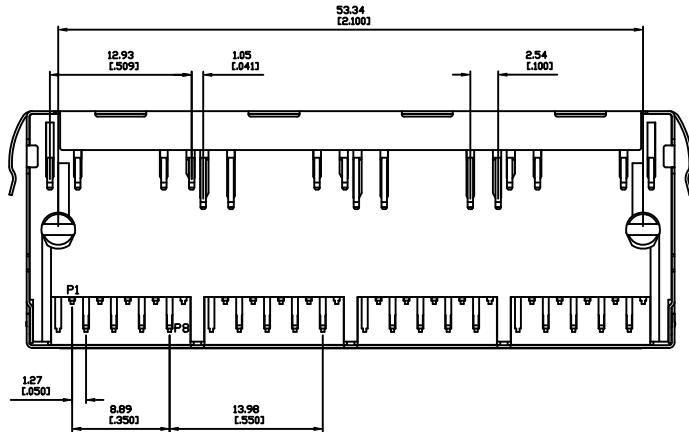
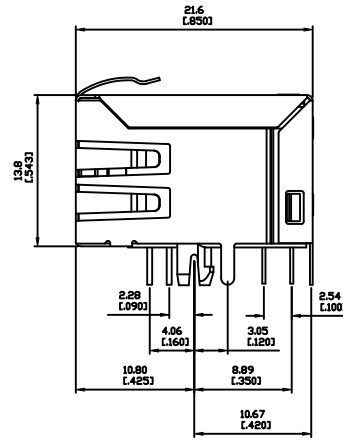
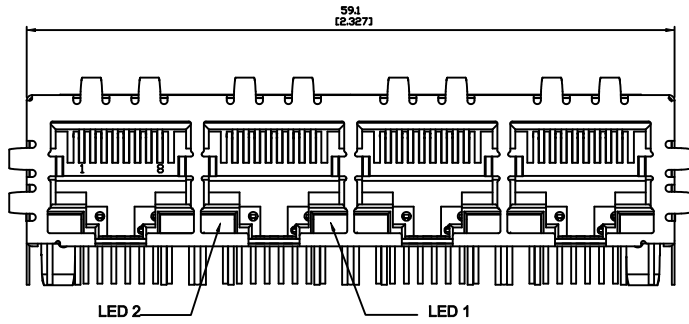


# 1. MECHANICAL DIMENSIONS:



- Material:**
- 1.Housing: Thermoplastic UL 94V-0
  - 2.Contact : Selective Gold Plating
  - 3.Terminal: 0.35mm Thickness Phosphor Bronze Plated With Gold And Tin In Solder Area.
  - 4.Shielded: Brass ,Nickel Plating
  - 5.Operating Temperature Range : 0°C ~70°C

**PART NO : GE8S - B\*4\*\* - D\*\* - 3**

Positions  
Contacts

Panel Ground  
Non : Non Panel Ground  
U : Top With Panel Ground  
D : 2 Sides With Panel Ground  
S : 3 Sides With Panel Ground

Housing Color  
B : Black

Gold Plated  
0 : Gold Flash  
1 : 6u"  
2 : 15u"  
3 : 30u"  
4 : 50u"

Ports  
Led Options  
Led Polarity

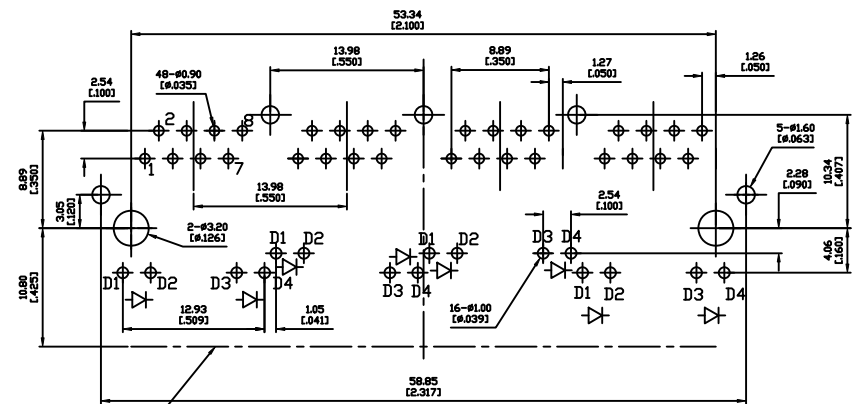
Circuit Type  
Protocol Type  
D: 10/100 Mbps,4 core

**LED Color Options Table**

LED Color Options		LED DASH OPTION
LED 2 (Left)	LED 1 (Right)	
No LED	No LED	0
Yellow	Green	1
No LED	Green	2
Yellow	No LED	3
Green	Yellow	4
Green	Green	5
Yellow	Yellow	6
Orange/Green	Orange/Green	7
Yellow/Green	Yellow/Green	8
Orange/Green	Yellow	9

**LED Polarity Table**  
(Applicable only for single-colored LED)

單位 編號	LED 2 (Left)	LED 1 (Right)
0	D1 <sup>+</sup> D2 <sup>-</sup>	D3 <sup>+</sup> D4 <sup>-</sup>
1	D1 <sup>-</sup> D2 <sup>+</sup>	D3 <sup>-</sup> D4 <sup>+</sup>
2	D1 <sup>+</sup> D2 <sup>+</sup>	D3 <sup>-</sup> D4 <sup>-</sup>
3	D1 <sup>-</sup> D2 <sup>-</sup>	D3 <sup>+</sup> D4 <sup>+</sup>



**RECOMMENDED PCB LAYOUT  
TOP VIEW**



**TACLEX**

**TACLEX ELECTRONICS CO., LTD.**

TOLERANCES UNLESS  
OTHERWISE SPECIFIED  
LINEAR: ±0.30 ANGULAR: ±3°

SIGNATURES	DATE	TITLE
DRAWN Eric	2010.12.17	RJ45 JACK +TSFM
CHK'D Alex	2010.12.17	NO.
APPV'D Rex	2010.12.17	GE88S-Bx4xx-Dxx-3

REVISIONS  
A



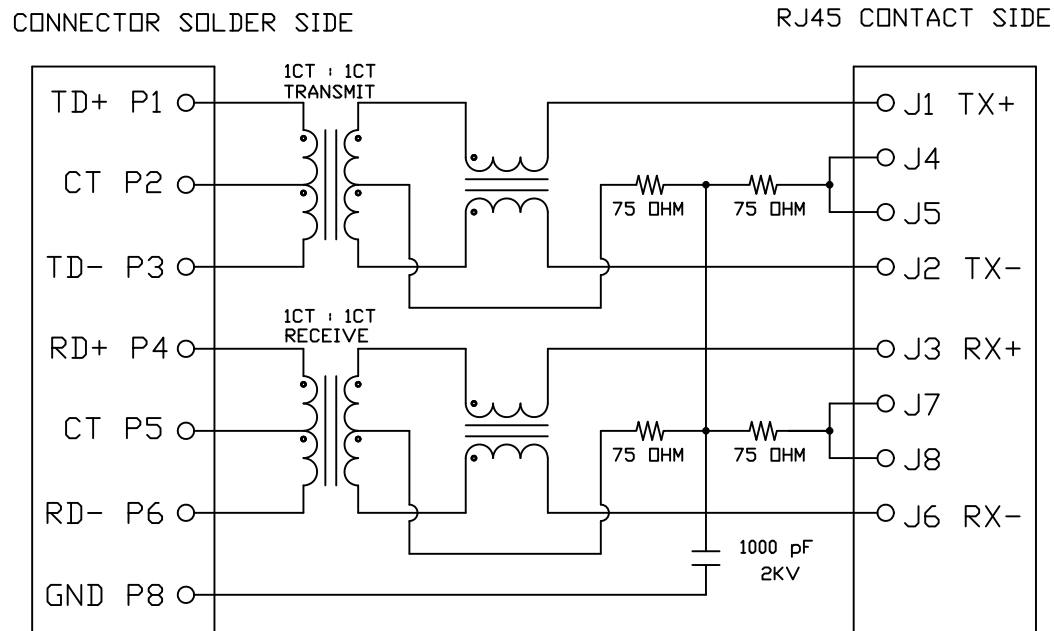
SCALE  
5/1

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△			
△			
SYM	DISCIPTION	DATE	APPROVED

1 2 3 4 5 6 7 8 9 10

# Circuit Type:D12

## 1. SCHEMATIC:



## 2. ELECTRICAL CHARACTERISTICS :

Test Notes:(25±5°C)

1.TR:(100KHz,0.1V):

Pins:(P1-P3):(J1-J2)=1:1±3%

Pins:(P4-P6):(J3-J6)=1:1±3%

2.LX:(100KHz,100mV,8mA, DC Bias)

Pins:(P1-P3),(P4-P6)=350uH Minimum

3.DCR:

Pins:(J1-J2),(J3-J6)=1.2 Ω Maximum

4.HIPOT:

Pins:(P1,P3)To(J1,J2)=1500VAC For 60s or 2250VDC 60s

Pins:(P4,P6)To(J3,J6)=1500VAC For 60s or 2250VDC 60s

5.INSERTION LOSS:

-1.0dB Maximum At 0.3MHZ To 100MHZ

6.RETURN LOSS:

-18dB Minimum At 1MHz To 30MHz,load 100 Ω ;

-16dB Minimum At 30MHz To 60MHz,load 100 Ω ;

-12dB Minimum At 60MHz To 80MHz,load 100 Ω .

7.CROSS TALK:

-30dB Minimum At 1MHz To 100MHz

8.COMMON TO COMMON MODE REJECTION:

-30dB Minimum At 1MHz To 100MHz

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△			
△			
SYM	DISCRIPTION	DATE	APPROVED



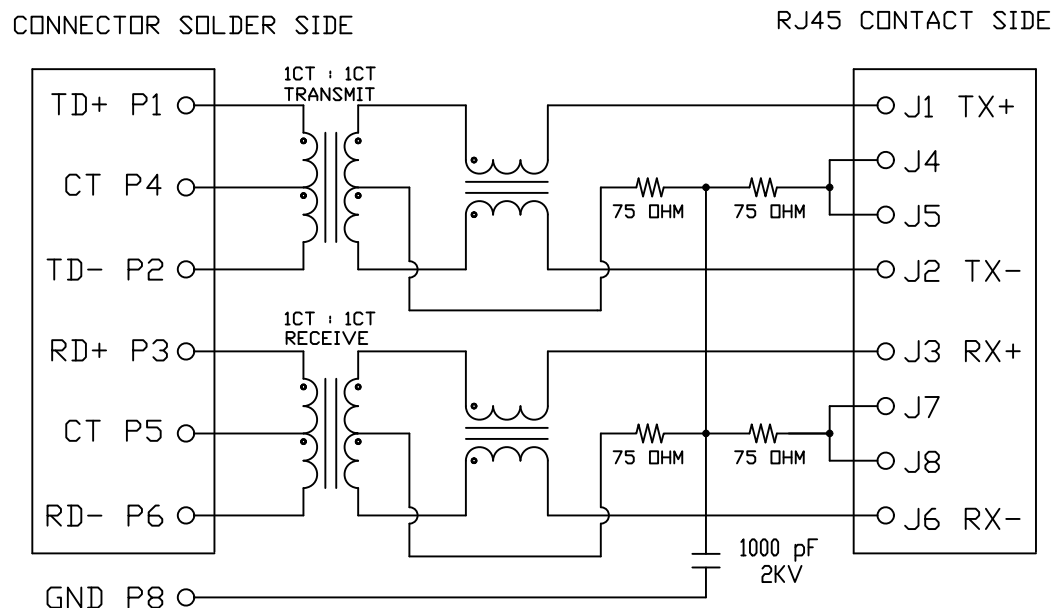
**TACLEX**

TACLEX ELECTRONICS CO., LTD.

TOLERANCES UNLESS OTHERWISE SPECIFIED LINEAR:±0.30 ANGULAR: ±3°	SIGNATURES		DATE	TITLE
	DRAWN	David	2009.03.16	Transformer Type
	CHK'D	Nick	2009.03.16	NO.
REVISIONS	SCALE	APPV'D		
A	5/1	Rex	2009.03.16	D12

# Circuit Type:D22

## 1. SCHEMATIC:



## 2. ELECTRICAL CHARACTERISTICS :

Test Notes:(25±5°C)

1.TR:(100KHz,0.1V):

Pins:(P1-P2):(J1-J2)=1:1±3%

Pins:(P3-P6):(J3-J6)=1:1±3%

2.LX:(100KHz,100mV,8mA, DC Bias)

Pins:(P1-P2),(P3-P6)=350uH Minimum

3.DCR:

Pins:(J1-J2),(J3-J6)=1.2 Ω Maximum

4.HIPOT:

Pins:(P1,P2)To(J1,J2)=1500VAC For 60s or 2250VDC 60s

Pins:(P3,P6)To(J3,J6)=1500VAC For 60s or 2250VDC 60s

5.INSERTION LOSS:

-1.0dB Maximum At 0.3MHZ To 100MHZ

6.RETURN LOSS:

-18dB Minimum At 1MHz To 30MHz,load 100 Ω ;

-16dB Minimum At 30MHz To 60MHz,load 100 Ω ;

-12dB Minimum At 60MHz To 80MHz,load 100 Ω .

7.CROSS TALK:

-30dB Minimum At 1MHz To 100MHz

8.COMMON TO COMMON MODE REJECTION:

-30dB Minimum At 1MHz To 100MHz

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△			
SYM	DISCRIPTION	DATE	APPROVED



**TACLEX**

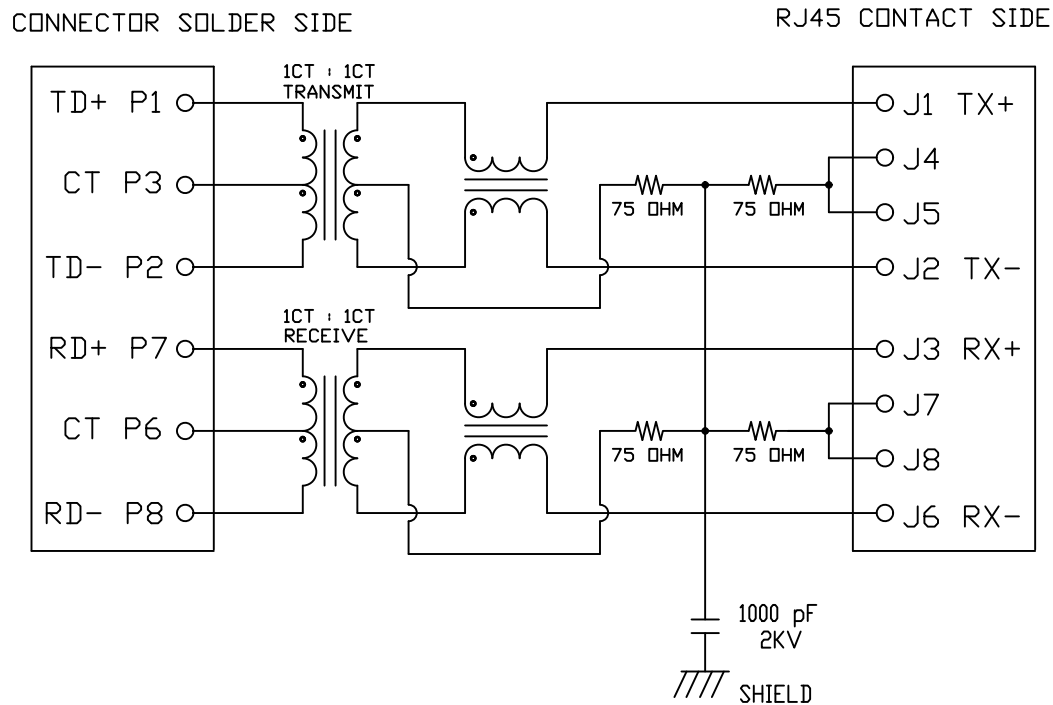
TACLEX ELECTRONICS CO., LTD.

TOLERANCES UNLESS OTHERWISE SPECIFIED LINEAR: ±0.30 ANGULAR: ±3°	
REVISIONS	SCALE
A	5/1

SIGNATURES		DATE	TITLE
DRAWN	David	2009.03.16	Transformer Type
CHK'D	Nick	2009.03.16	NO.
APPV'D	Rex	2009.03.16	D22

# Circuit Type:D31

## 1. SCHEMATIC:



## 2. ELECTRICAL CHARACTERISTICS :

Test Notes:(25±5°C)

1.TR:(100KHz,0.1V):

Pins:(P1-P2):(J1-J2)=1:1±3%

Pins:(P7-P8):(J3-J6)=1:1±3%

2.LX:(100KHz,100mV,8mA, DC Bias)

Pins:(P1-P2),(P7-P8)=350uH Minimum

3.DCR:

Pins:(J1-J2),(J3-J6)=1.2 Ω Maximum

4.HIPOT:

Pins:(P1,P2) To (J1,J2)=1500VAC For 60s or 2250VDC 60s

Pins:(P7,P8) To (J3,J6)=1500VAC For 60s or 2250VDC 60s

5.INSERTION LOSS:

-1.0dB Maximum At 0.3MHZ To 100MHZ

6.RETURN LOSS:

-18dB Minimum At 1MHz To 30MHz,load 100 Ω ;

-16dB Minimum At 30MHz To 60MHz,load 100 Ω ;

-12dB Minimum At 60MHz To 80MHz,load 100 Ω .

7.CROSS TALK:

-30dB Minimum At 1MHz To 100MHz

8.COMMON TO COMMON MODE REJECTION:

-30dB Minimum At 1MHz To 100MHz

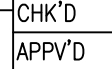
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SYM	DISCRIPTION	DATE	APPROVED



**TACLEX**

TACLEX ELECTRONICS CO., LTD.

TOLERANCES UNLESS OTHERWISE SPECIFIED LINEAR: ±0.30 ANGULAR: ±3°		SIGNATURES		DATE	TITLE
REVISIONS	SCALE	DRAWN	David	2009.03.16	Transformer Type
A	5/1	CHK'D	Nick	2009.03.16	
		APPV'D	Rex	2009.03.16	

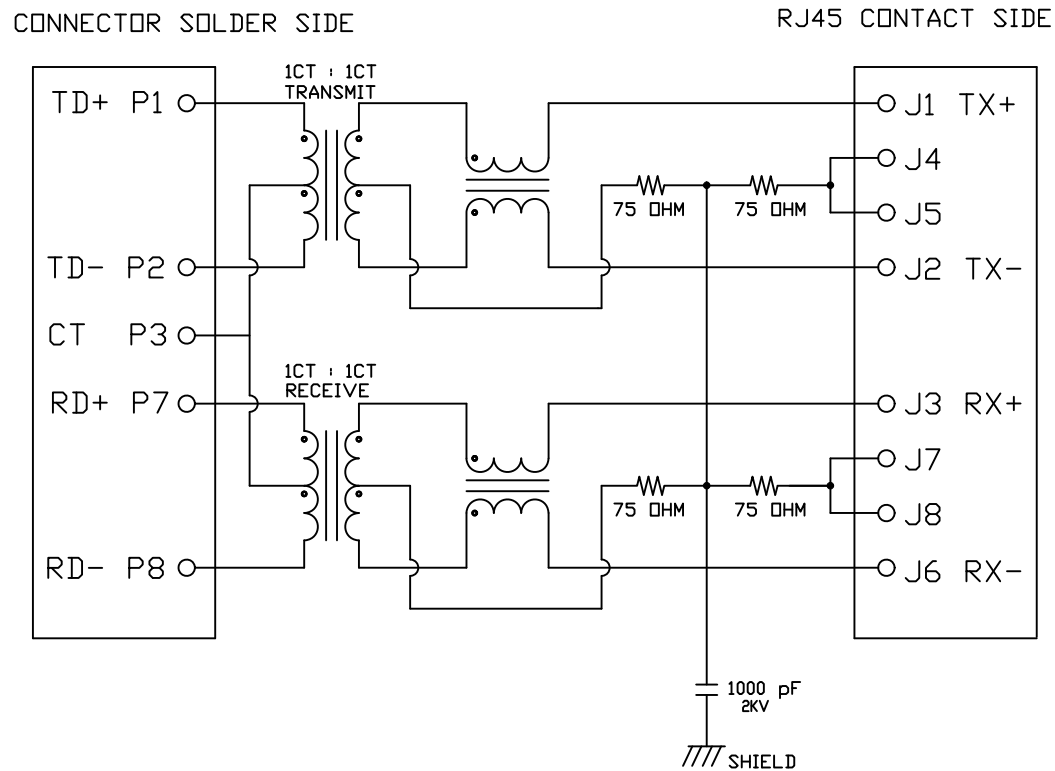


NO.

D31

# Circuit Type:D41

## 1. SCHEMATIC:



## 2. ELECTRICAL CHARACTERISTICS :

Test Notes:(25±5°C)

1.TR:(100KHz,0.1V):

Pins:(P1-P2):(J1-J2)=1:1±3%

Pins:(P7-P8):(J3-J6)=1:1±3%

2.LX:(100KHz,100mV,8mA, DC Bias)

Pins:(P1-P2),(P7-P8)=350uH Minimum

3.DCR:

Pins:(J1-J2),(J3-J6)=1.2 Ω Maximum

4.HIPOT:

Pins:(P1,P2)To(J1,J2)=1500VAC For 60s or 2250VDC 60s

Pins:(P7,P8)To(J3,J6)=1500VAC For 60s or 2250VDC 60s

5.INSERTION LOSS:

-1.0dB Maximum At 0.3MHZ To 100MHZ

6.RETURN LOSS:

-18dB Minimum At 1MHz To 30MHz,load 100 Ω ;

-16dB Minimum At 30MHz To 60MHz,load 100 Ω ;

-12dB Minimum At 60MHz To 80MHz,load 100 Ω .

7.CROSS TALK:

-30dB Minimum At 1MHz To 100MHz

8.COMMON TO COMMON MODE REJECTION:

-30dB Minimum At 1MHz To 100MHz

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△			
△			
△			
△			
SYM	DISCRIPTION	DATE	APPROVED



**TACLEX**

TACLEX ELECTRONICS CO., LTD.

TOLERANCES UNLESS OTHERWISE SPECIFIED LINEAR: ±0.30 ANGULAR: ±3°	SIGNATURES		DATE	TITLE
	DRAWN	David	2009.03.16	Transformer Type
	CHK'D	Nick	2009.03.16	NO.
REVISIONS	SCALE	APPV'D		
A	5/1	Rex	2009.03.16	D41